

BEE BRANCH SOLID-STATE CIRCUITS DAY

Where ICs are in IEEE
Friday 28th of June

Program:

Vendredi 28 Juin 2019 – Laboratoire IMS / Amphi JP DOM

09h00 – 09h30

Welcome Coffee

09h30 – 10h20

Prof. Asad Abidi, University of California Los Angeles
IEEE Fellow & TWAS Fellow

“A Triple-Loop CMOS PLL that Synthesizes Frequencies with Low Jitter and Spurs, Without Calibration”

10h20 – 11h10

Prof. Ramesh Harjani, University of Minnesota
IEEE Fellow

“Integrated DC-DC Converter Designs”

11h10 – 12h00

Dr. Stefano Pellerano, Principal Engineer, Intel Labs
RFIC 2019 General Chair

“Quantum Computing: An IC Design Perspective”

12h00

Luncheon Buffet